

PTO/SB/08B(10-01)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete if Known			
Application Number	09/542,783		
Filing Date	April 4, 2000		
First Named Inventor	Whitman et al.		
Group Art Unit	2823		
Examiner Name	B. Kebede		
Attorney Docket Number	2269-4294US (98-1208 00/US)		

		OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS	
Examiner Initials *	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
BU		VAN ZANT, Peter, "Microchip Fabrication, A Practical Guide to Semiconductor Processing," pp. 176-178 and 185-187.	
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Examiner Signature	Brook Kehede	Date Considered	Centenhu	(7, 101)
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Substitute for form 1449 PRANCE INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet	1	of		Attorney Docket Number	2269-4294US (98-1208,00/US)

]	Document Number	Publication Date	Name of Patentee or Applicant of	Pages, Columns, Lines, Where Relevant
Examiner Initials *	Cite No.1	Number - Kind Code ² (if known)	MM-DD-YYYY	Cited Document	Pages, Columbis, Lines, where Relevant Passages or Relevant Figures Appear
BU		US-6,268,246 B1	07/2001	Ukita et al.	•
Bol		US- 6,358,793 B1	03/2002	Yates et al.	
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